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of

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Complete if Known

Application Number	10/029,502
Filing Date	December 21, 2001
First Named Inventor	Luo, Fa-Long, et. al.
Art Unit	2109-2121
Examiner Name	Unassigned
Attorney Docket Number	021202-002300US

U.S. PATENT DOCUMENTS

Examiner	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Class	Subclass
		Number Kind Code ² (if known)			Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
C.D.	AA	US-5,517,600	05-14-1996	Shimokawa	706	15
C.D.	AB	US-5,720,002	02-17-1998	Wang	706	25
C.D.	AC	US-6,041,322	03-21-2000	Meng et al.	706	43

FOREIGN PATENT DOCUMENTS

Examiner Initials [*]	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ³
		Country Code ³	Number ⁴	Kind Code ⁵ (if known)				
C.D.	AD	EP	0479102	A2	04-08-1992			<input type="checkbox"/>

NON PATENT LITERATURE DOCUMENTS

Examiner Initials [*]	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
C.D.	AE	Mascia et al., "Neural Net Implementation on Single-Chip Digital Signal Processor", IEEE, XP010091309 (1989).	

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